

What is claimed is:

1. An image display device comprising:
a plurality of pixels which are arranged in matrix;
5 a plurality of data signal lines;
a plurality of scanning lines;
a first driver circuit which controls the data signal lines; and
a second driver circuit which controls the scanning lines, and
a testing circuit comprising a plurality of NAND circuits connected in series;
10 wherein each of the plurality of data signal lines is connected to each of input
portions of the plurality of NAND circuits;
wherein an output portion of the testing circuit is connected to a testing
terminal and an input portion of the testing circuit is connected to a power source, and
wherein the first driver circuit and the plurality of pixels are connected to the
15 testing circuit through the data signal line.

2. A image display device according to claim 1, wherein an electronic device
mounting the image display device is any one of a laptop personal computer, a portable
information terminal, a video camera, a cellular phone, a digital camera.

20 3. A testing method of an image display device:
a plurality of pixels which are arranged in matrix;
a plurality of data signal lines;
a plurality of scanning lines;
25 a first driver circuit which controls the data signal lines; and
a second driver circuit which controls the scanning lines, and
a testing circuit comprising a plurality of NAND circuits connected in series;
wherein each of the plurality of data signal lines is connected to each of input
portions of the plurality of NAND circuits;
30 wherein an output portion of the testing circuit is connected to a testing
terminal and an input portion of the testing circuit is connected to a power source;
wherein the first driver circuit and the plurality of pixels are connected to the
testing circuit through the data signal line, and
wherein a testing pulse is inputted to the testing circuit and a square wave
35 signal is supplied to an output portion of the testing terminal in accordance with the
testing pulse.

4. A testing method of an image display device according to claim 3:
wherein the testing pulse is outputted to the data signal line in accordance with
the input of a video signal.

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5. A testing method of an image display device according to claim 3:
wherein the testing pulse is a High signal in all the data signal lines and is
switched sequentially into a Low signal.

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6. A testing method of an image display device according to claim 3:
wherein the testing pulse is inputted simultaneously to the NAND circuits
connected in series.

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7. An image display device comprising:
a plurality of data signal lines;
a plurality of scanning lines extending orthogonally to said plurality of gate lines;
a plurality of pixels surrounded by said plurality of gate lines and said plurality of
source lines;
a first driver circuit which controls the data signal lines; and
a second driver circuit which controls the scanning lines, and
a testing circuit comprising a plurality of NAND circuits connected in series;
wherein each of the plurality of data signal lines is connected to each of input
portions of the plurality of NAND circuits;
wherein an output portion of the testing circuit is connected to a testing
terminal and an input portion of the testing circuit is connected to a power source, and
wherein the first driver circuit is connected to the testing circuit through the
data signal line.

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8. A image display device according to claim 7, wherein an electronic device
mounting the image display device is any one of a laptop personal computer, a portable
information terminal, a video camera, a cellular phone, a digital camera.

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9. A testing method of an image display device:
a plurality of data signal lines;
a plurality of scanning lines extending orthogonally to said plurality of gate lines;
a plurality of pixels surrounded by said plurality of gate lines and said plurality of

source lines;

a first driver circuit which controls the data signal lines; and

a second driver circuit which controls the scanning lines, and

a testing circuit comprising a plurality of NAND circuits connected in series;

5 wherein each of the plurality of data signal lines is connected to each of input portions of the plurality of NAND circuits;

wherein an output portion of the testing circuit is connected to a testing terminal and an input portion of the testing circuit is connected to a power source,

10 wherein the first driver circuit is connected to the testing circuit through the data signal line, and

wherein a testing pulse is inputted to the testing circuit and a square wave signal is supplied to an output portion of the testing terminal in accordance with the testing pulse.

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10. A testing method of an image display device according to claim 9:

wherein the testing pulse is outputted to the data signal line in accordance with the input of a video signal.

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11. A testing method of an image display device according to claim 9:

wherein the testing pulse is a High signal in all the data signal lines and is switched sequentially into a Low signal.

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12. A testing method of an image display device according to claim 9:

25 wherein the testing pulse is inputted simultaneously to the NAND circuits connected in series.

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